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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>		Application / Conf. No.	10/796,475 / 2300
		Filing Date	March 09, 2004
		First Named Inventor	Stephen M. Trimberger
		Examiner Name	Saqib Javaid Siddiqui
		Art Unit	2136
Mail Stop: AMENDMENT		Patent No.	
Express Mail Receipt No.		Attorney Docket Number	X-812-1P US
Total Number of Pages in This Submission			

ENCLOSURES (check all that apply)

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Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Customer Number	24309	Reg. Number 37,652
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Date	June 14, 2006	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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IN THE UNITED STATES PATENT OFFICE



Applicants: Stephen M. Trimberger et al.
Assignee: Xilinx, Inc.
Title: "FPGA Configuration Memory with Built-In Error Correction Mechanism"
Serial No.: 10/796,475 File Date: 03-09-04
Examiner: Saqib Javaid Siddiqui Art Unit: 2138
Docket No.: X-812-1P US Conf. No.: 2300

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT AND RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

This amendment is in response to the First Office Action, mailed from the Patent Office on April 4, 2006.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.